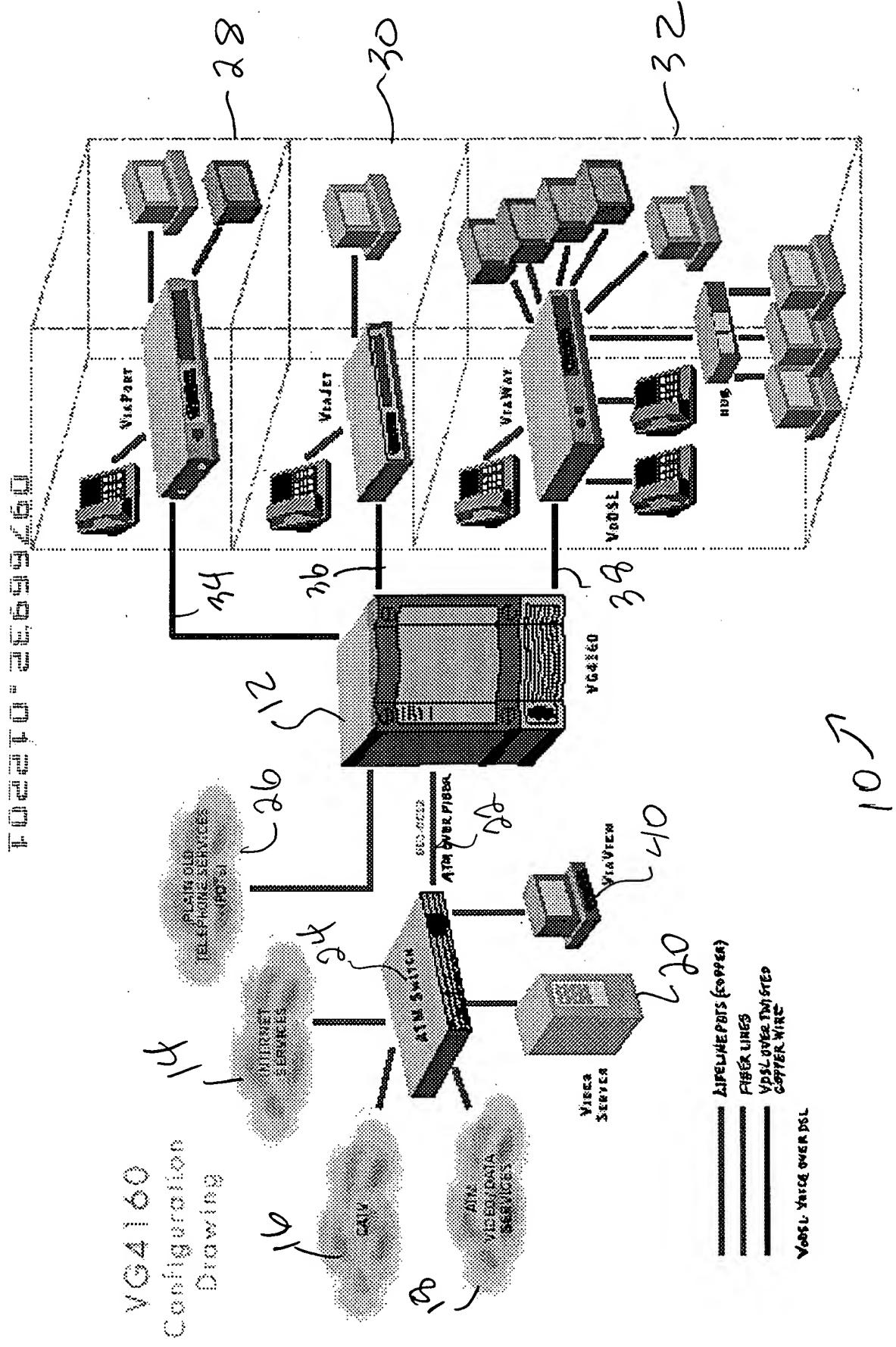
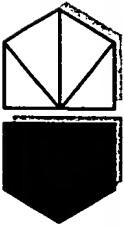


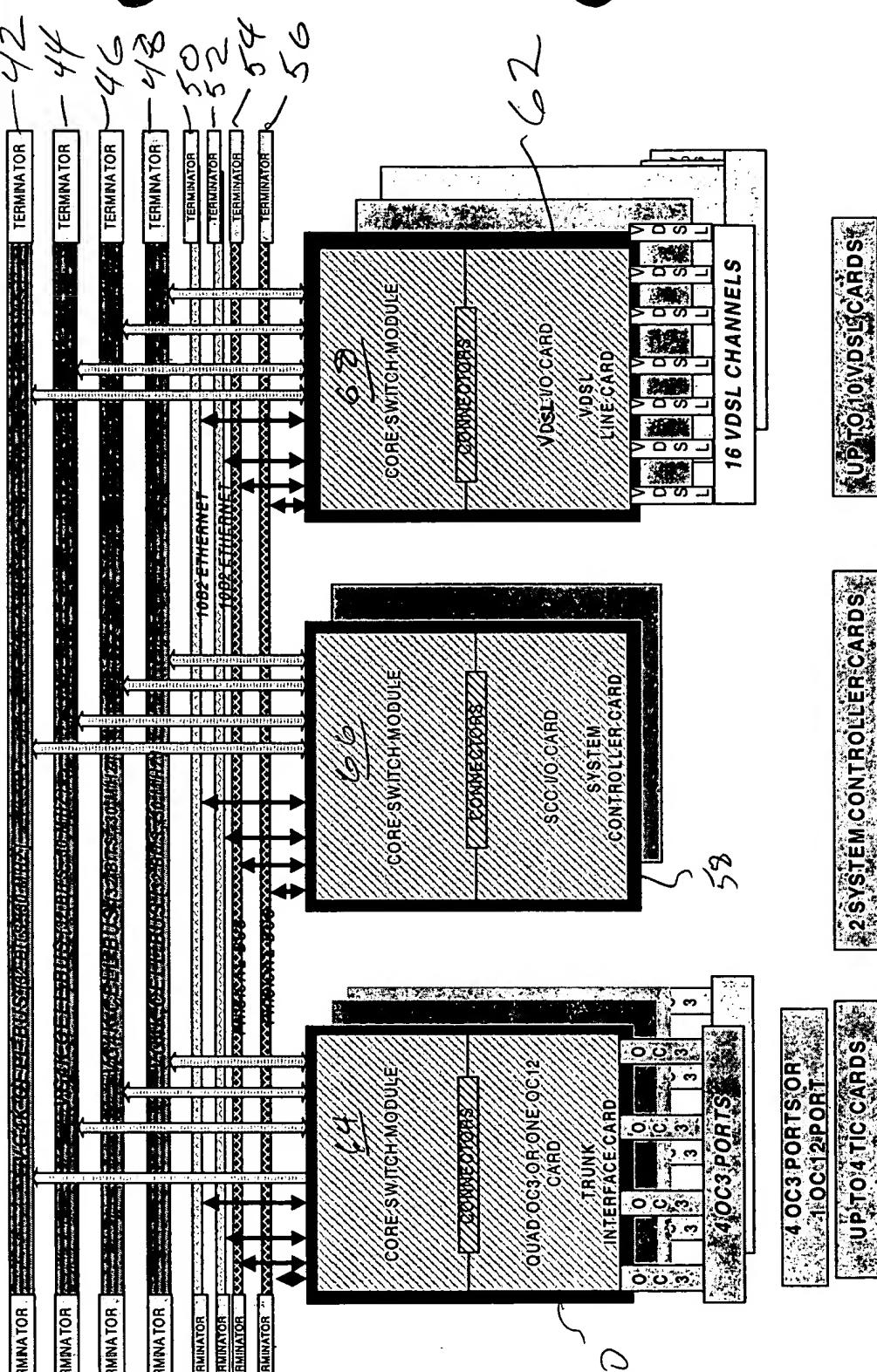
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Fig. 1





VG4000⁺ Block Diagram



VG 4000⁺ ARCHITECTURE

Fig. 2



CSM Block Diagram

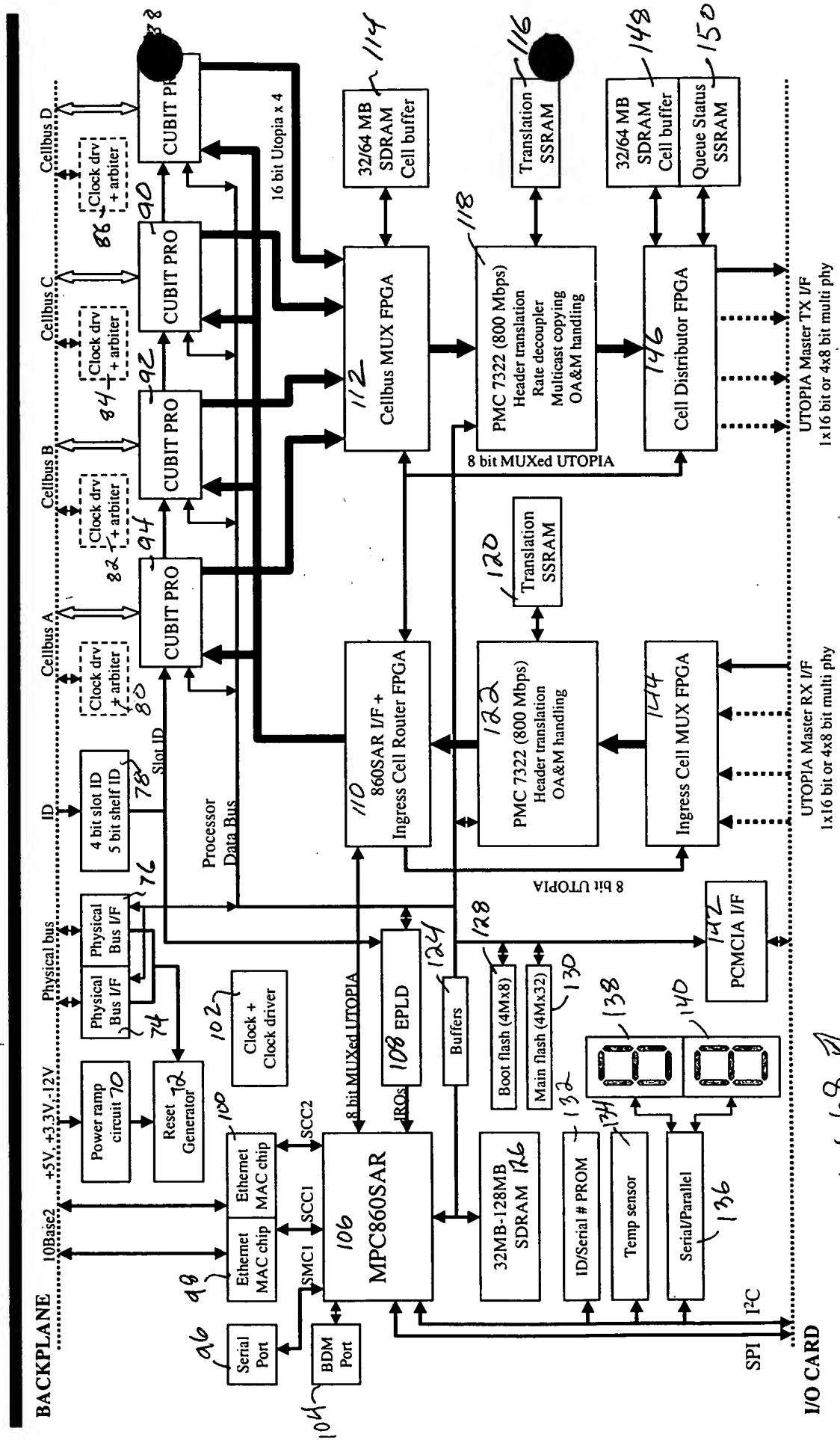
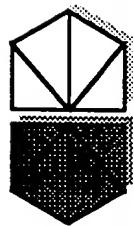


Fig. 3

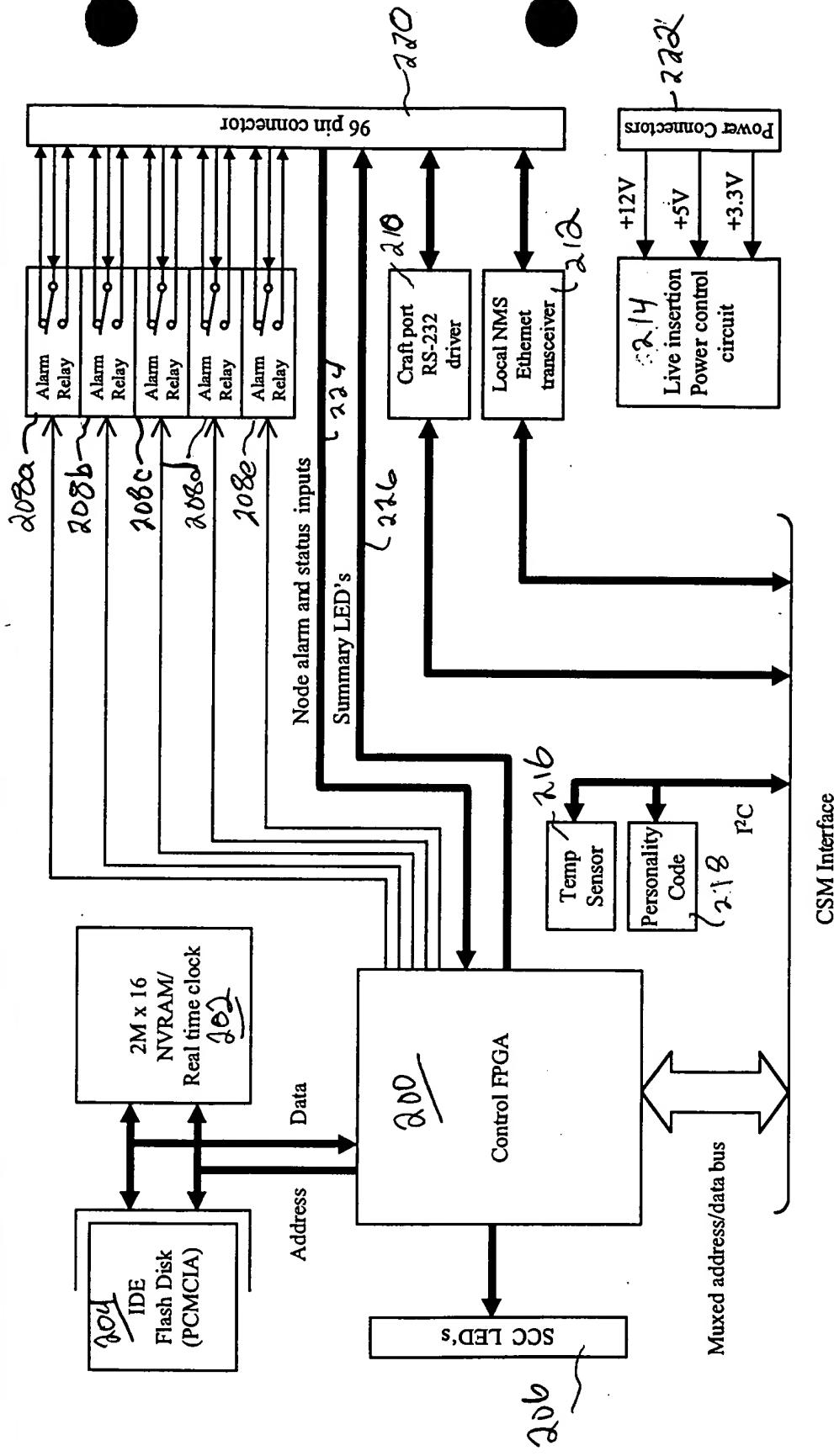
64,160,687

UTOPIA Master RX I/F
1x16 bit or 4x8 bit multi phy

UTOPIA Master TX I/F
1x16 bit or 4x8 bit multi phy

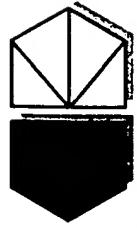


System Controller I/O Card

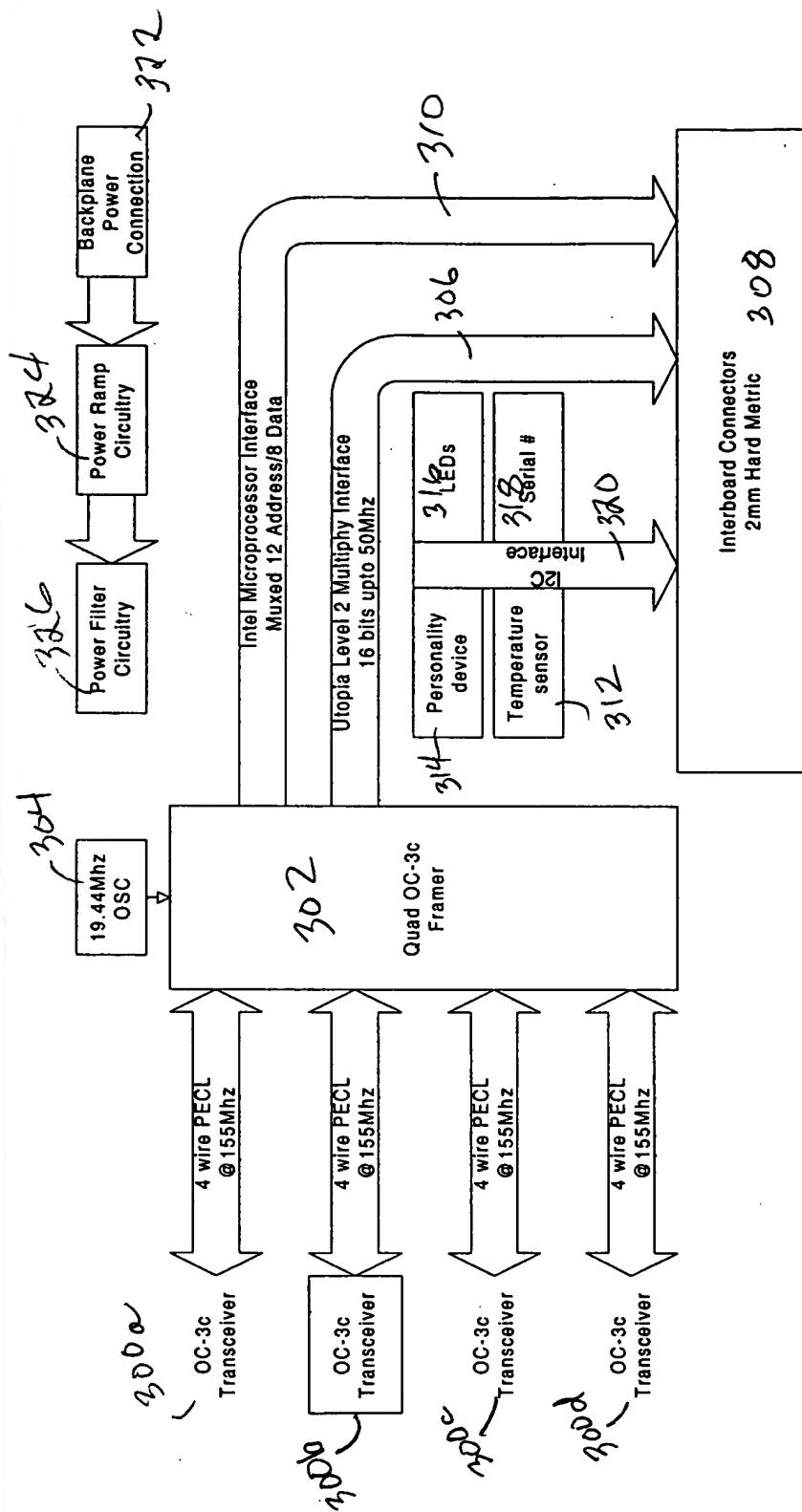


58

Fig. 4



Quad OC3 Block Diagram



60 →

Fig. 5

VLC - VDSL Line Card

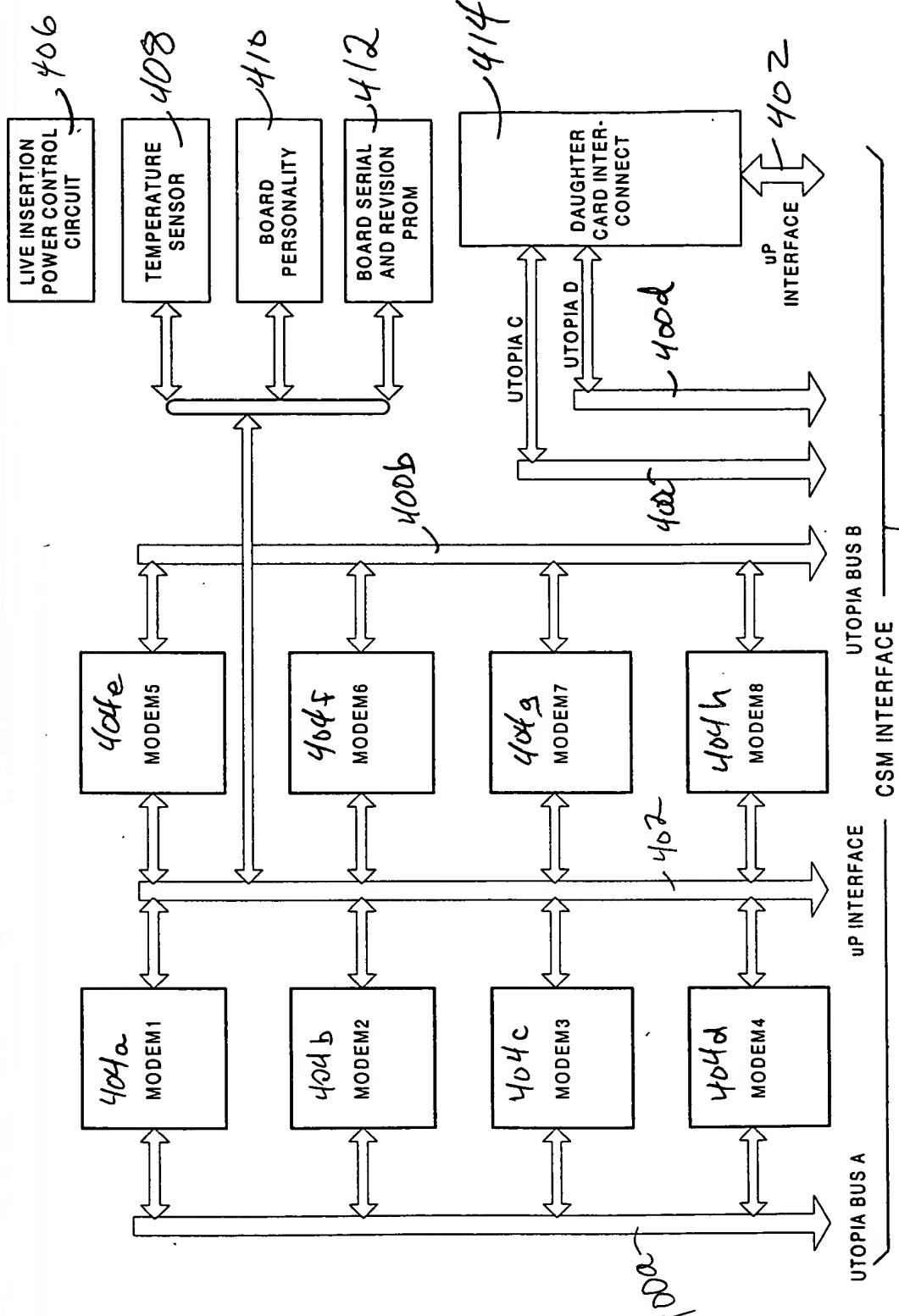
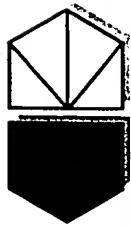
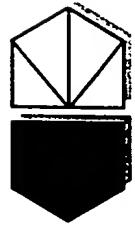


Fig. 6



ViaJet A40X Block Diagram

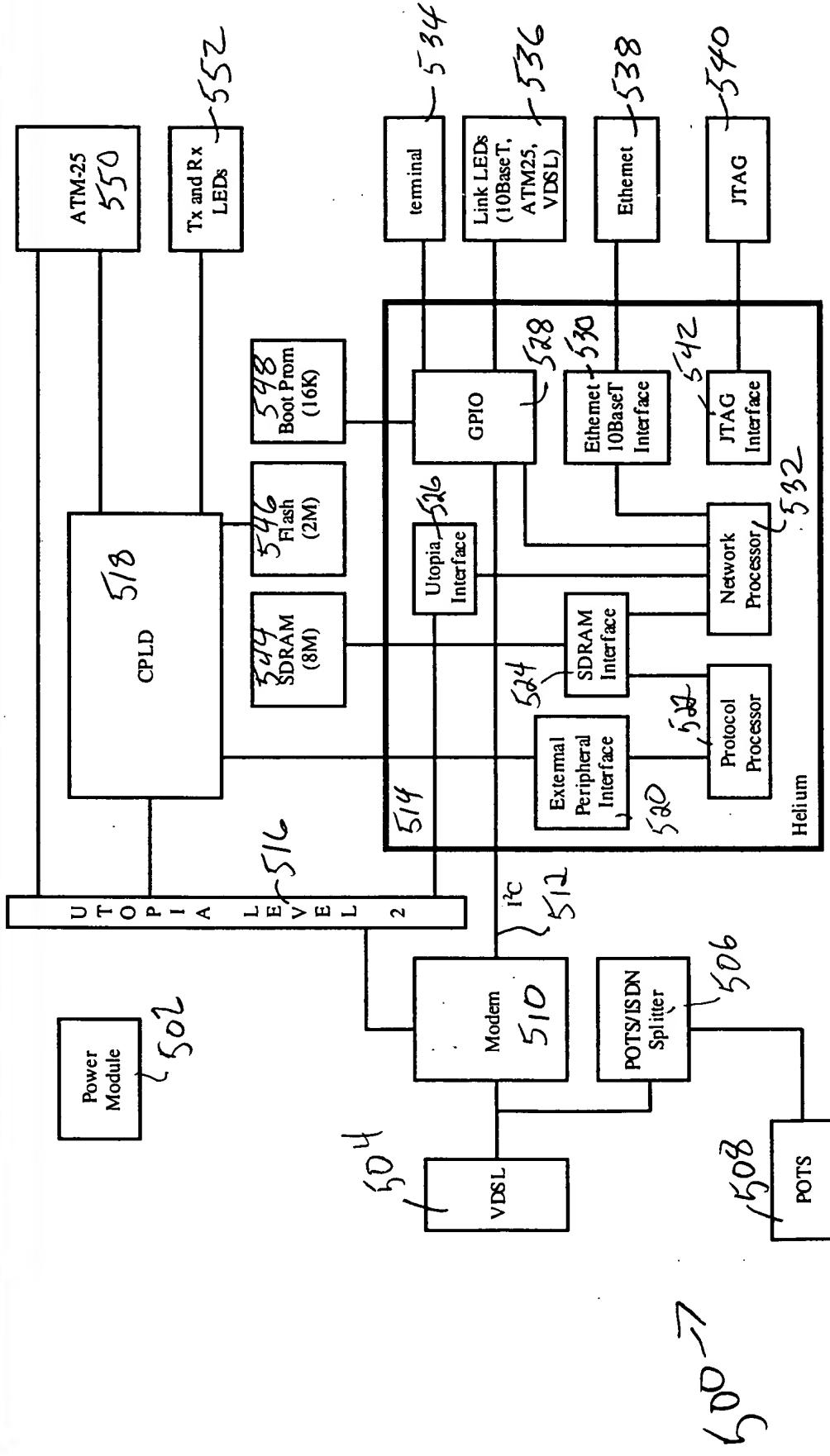
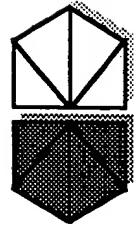


Fig. 7a



ViaWay A406

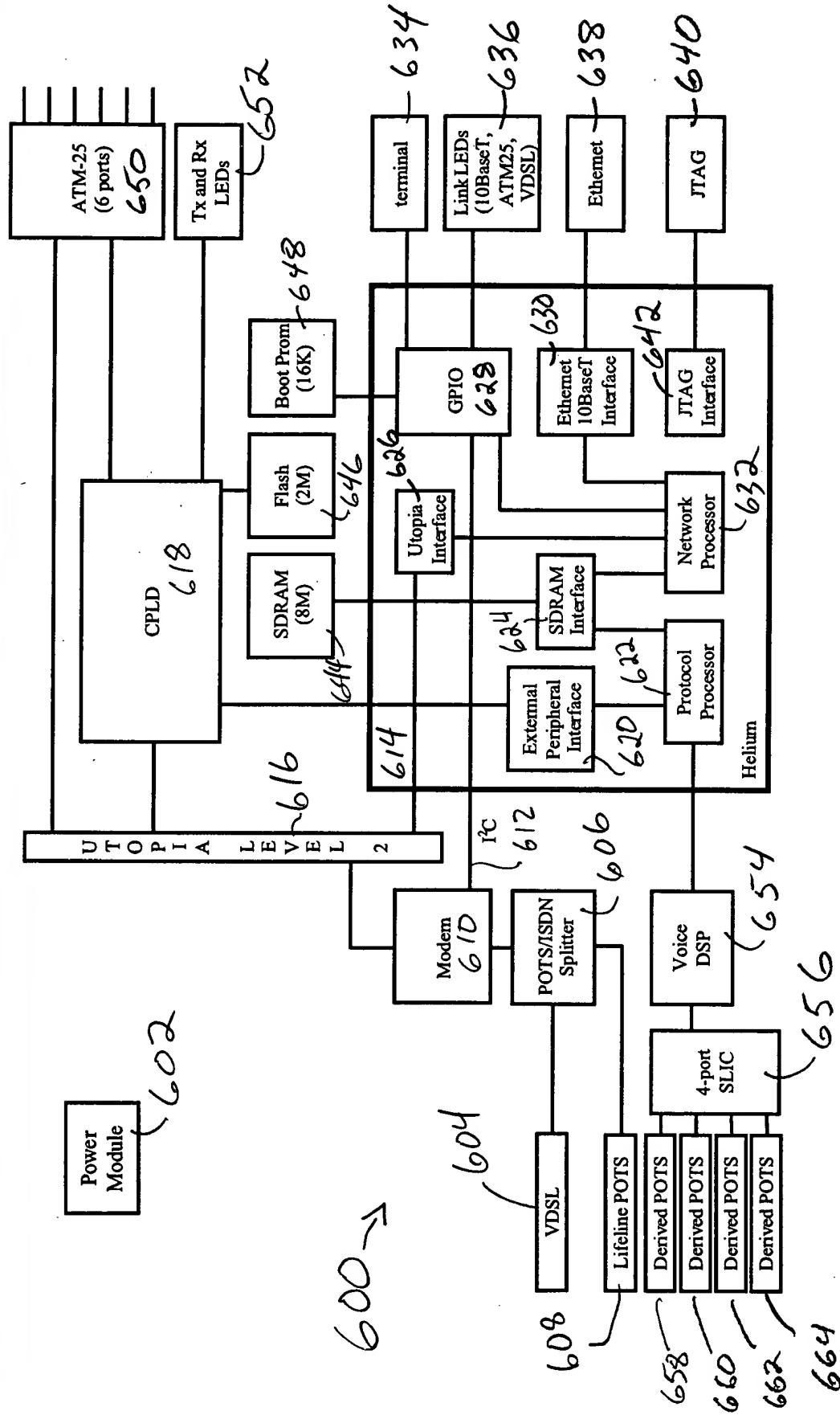
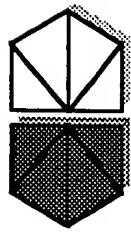


Fig. 7b



Typical Digital Set-Top Box

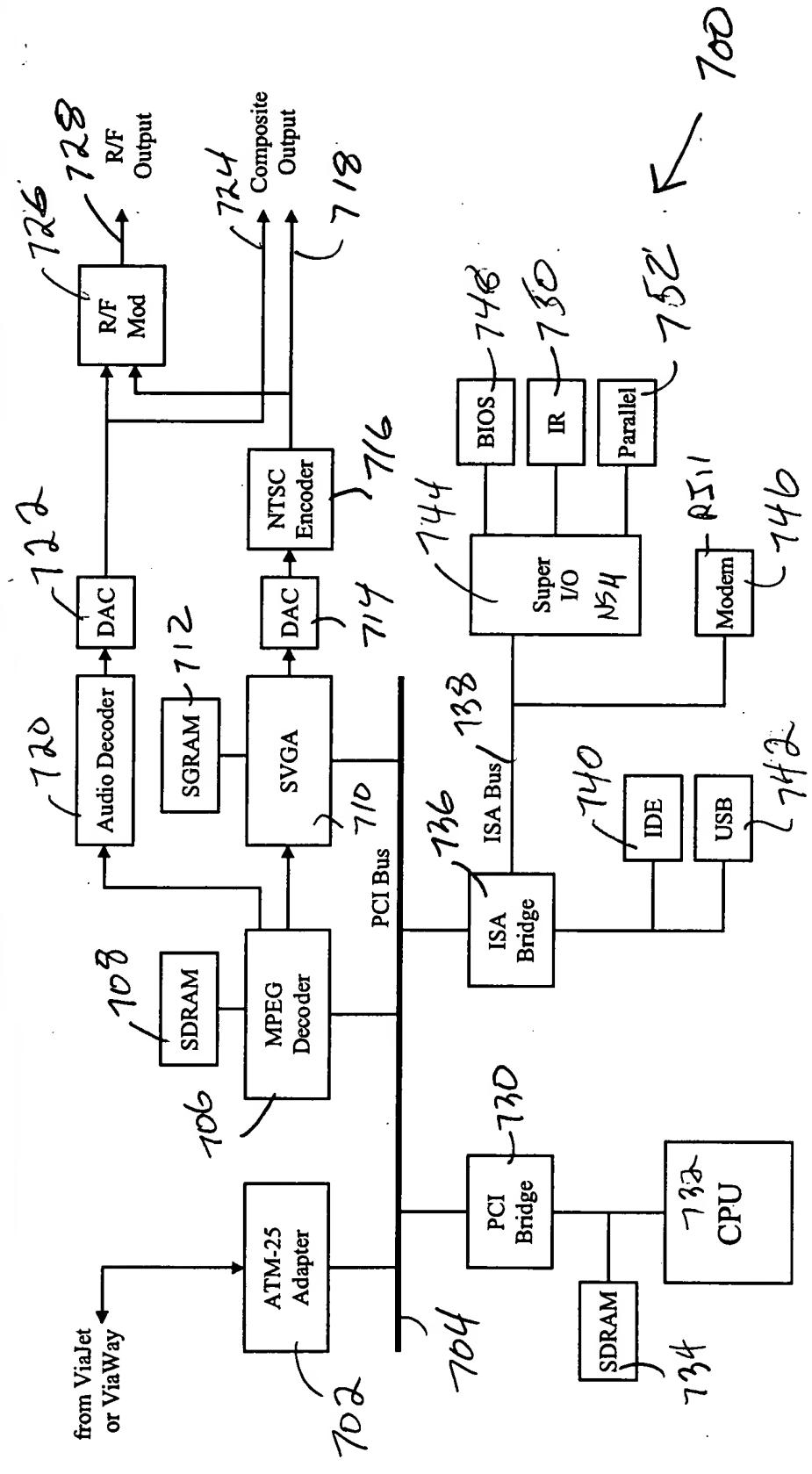


Fig. 7c

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802

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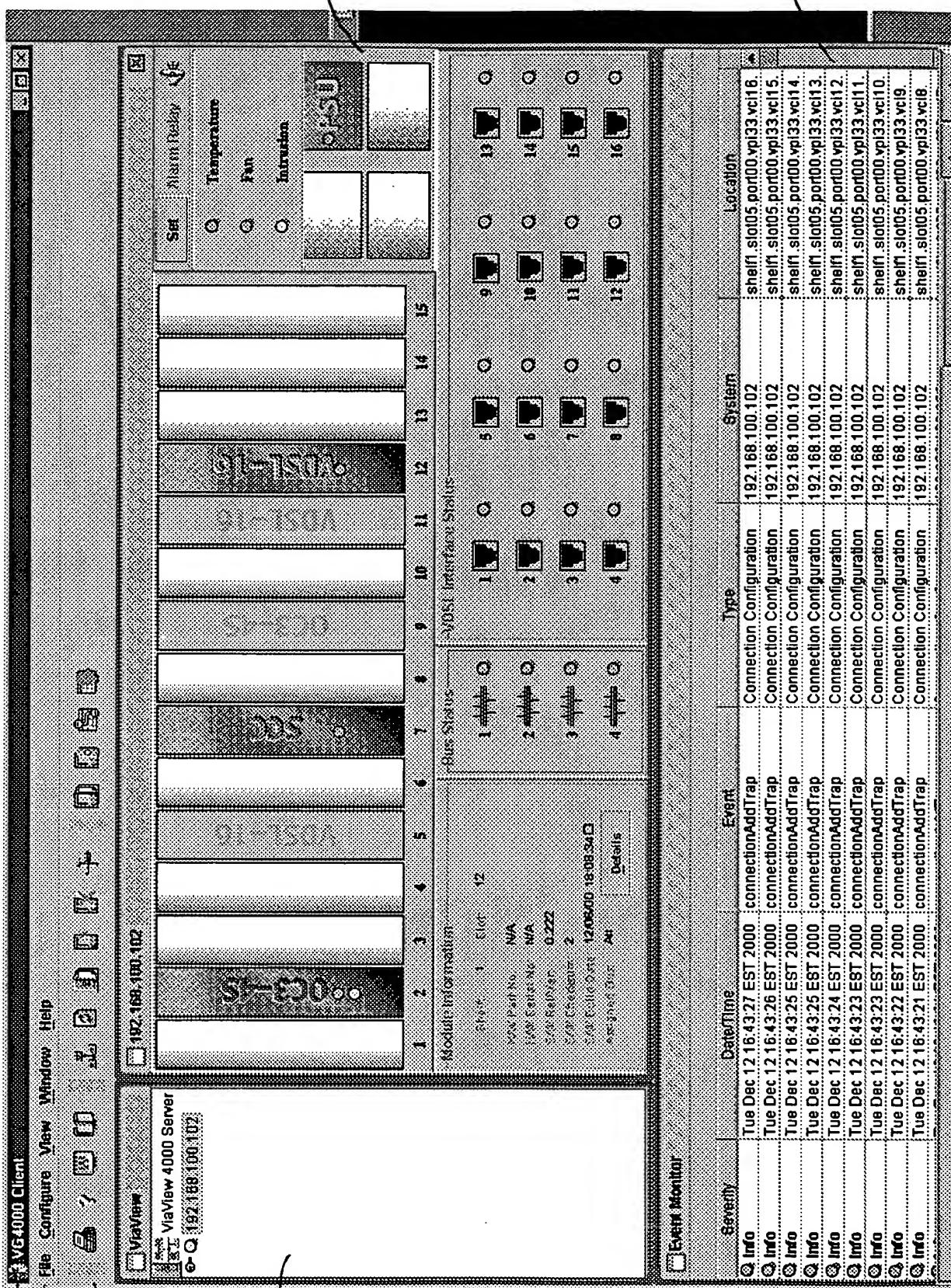
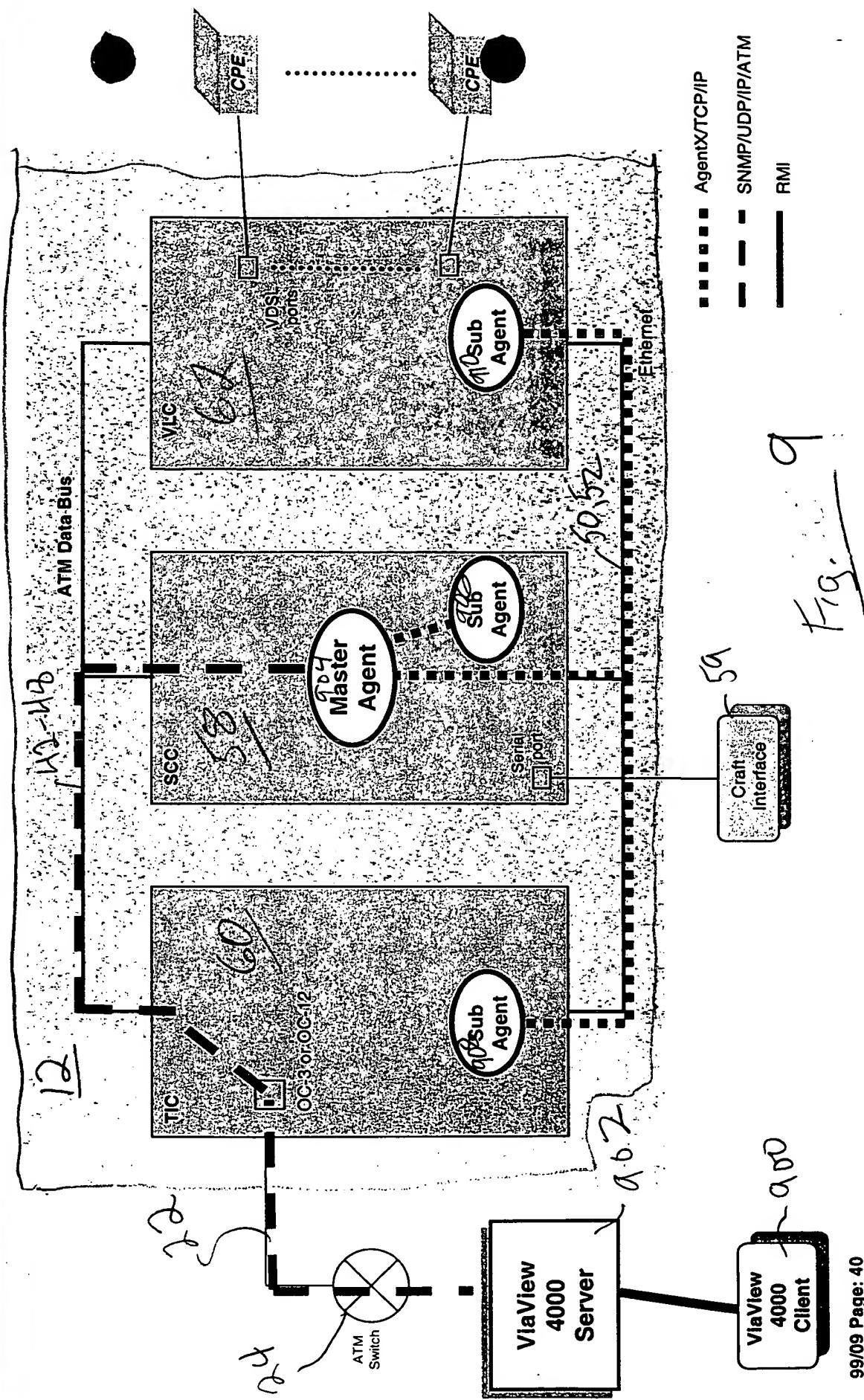
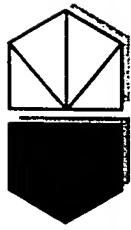


Fig. 8

Node Management Paths



99/09 Page: 40

ViaView
4000
Client

59

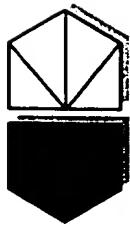
Craft Interface

RMII

SNMP/UDP/IP/ATM

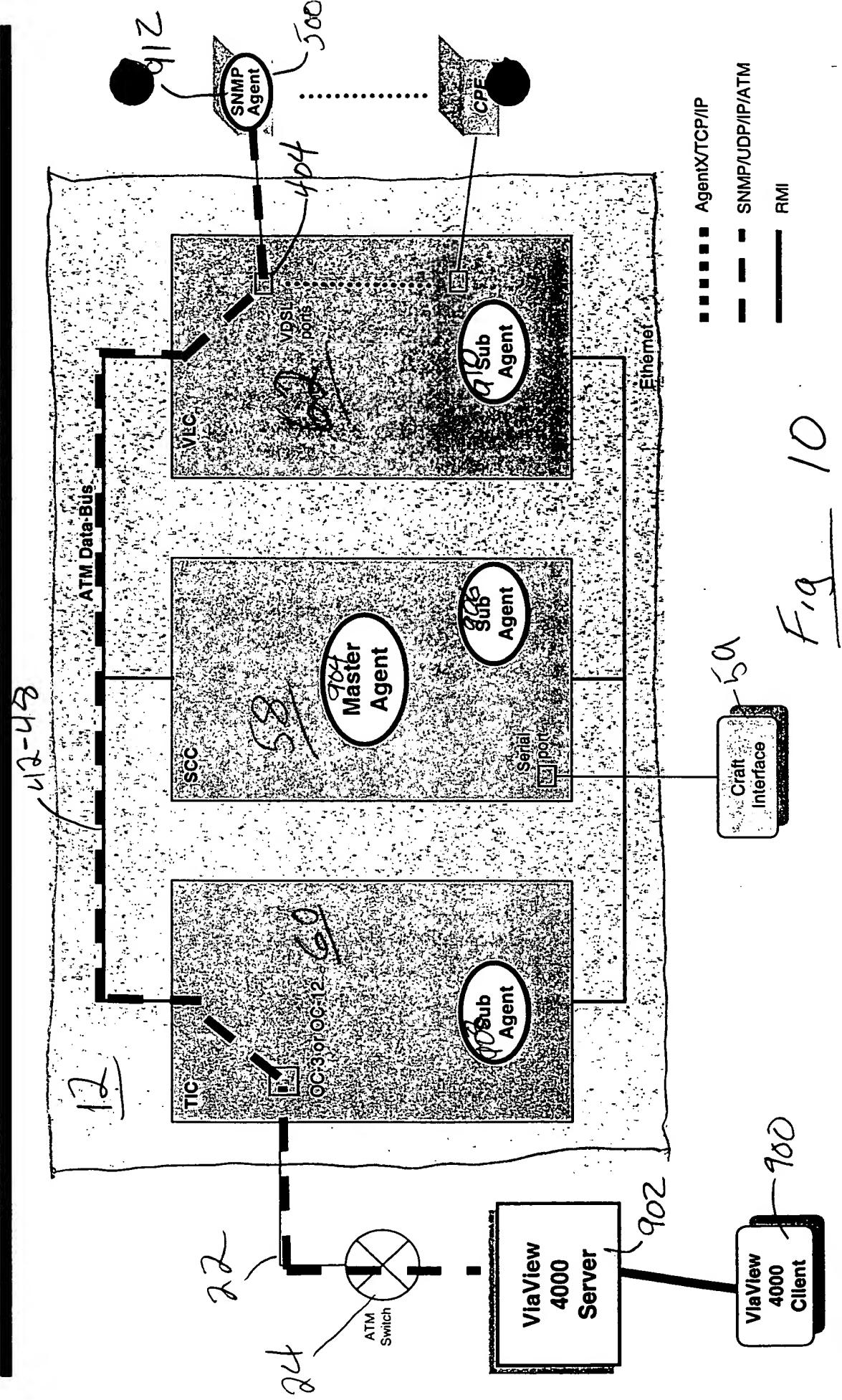
Agencia ICP/IF

Fig. q



CPE Management Path

四庫全書



Connection Management

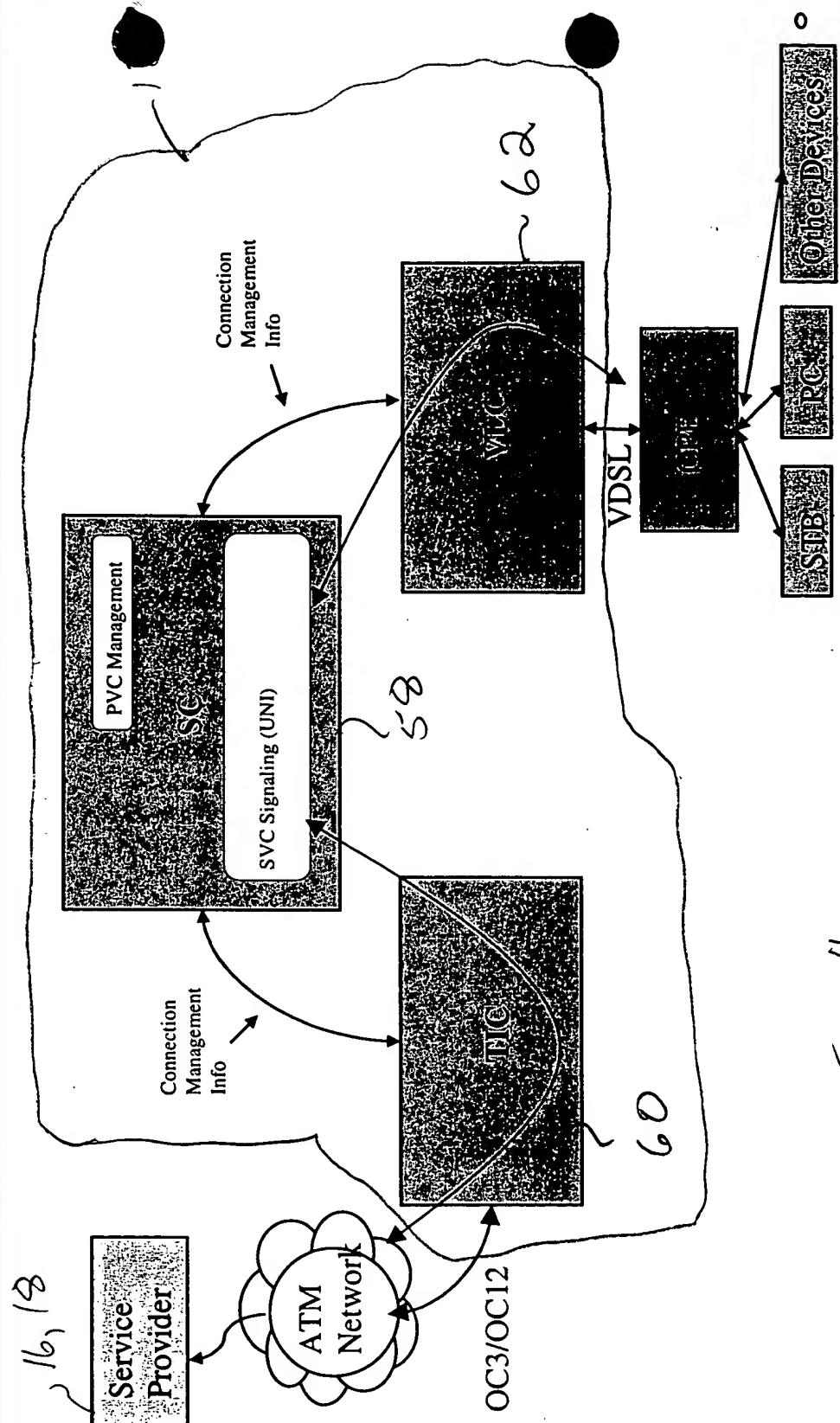
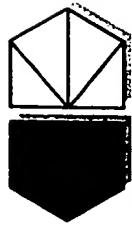
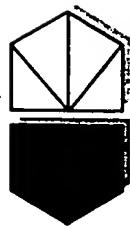


Fig. 11



CATV/Broadcast Management

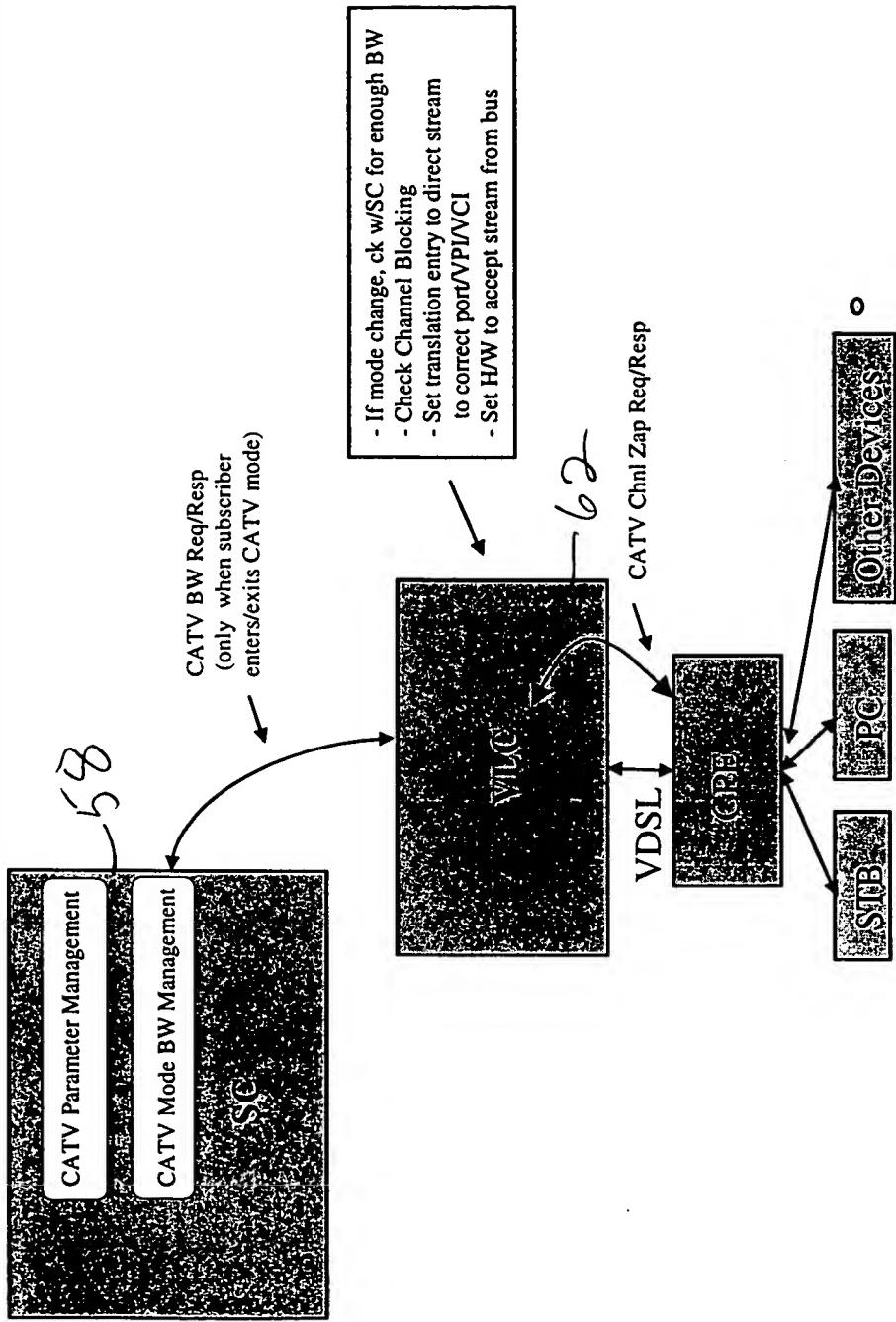


Fig. 12

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